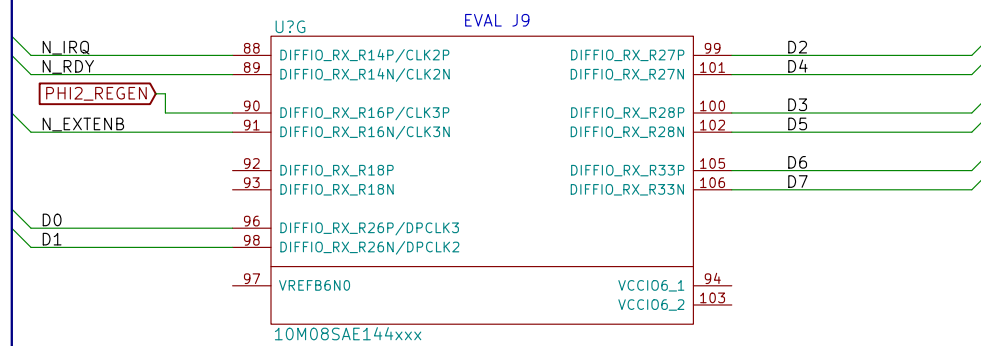
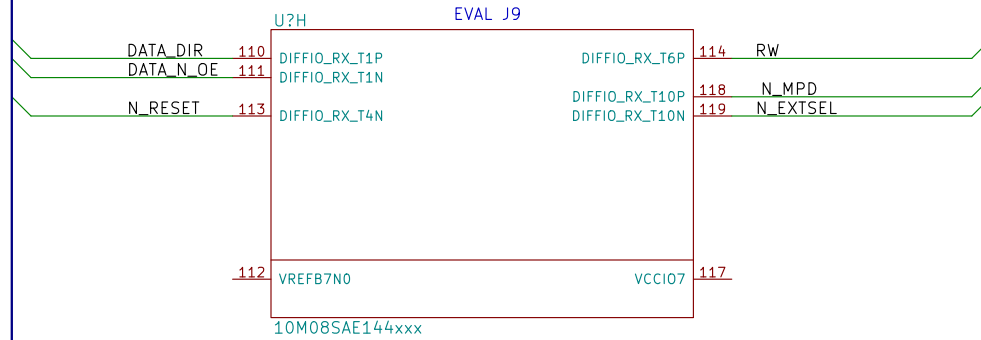
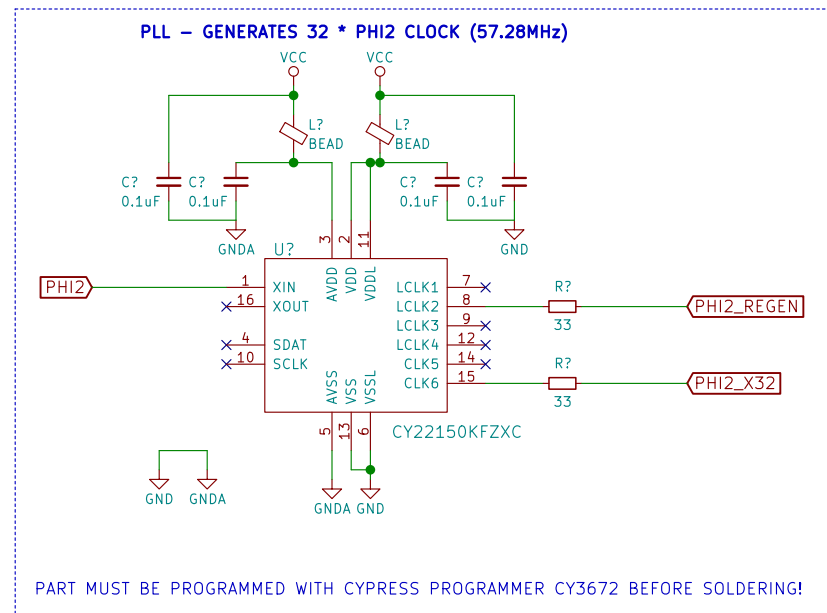


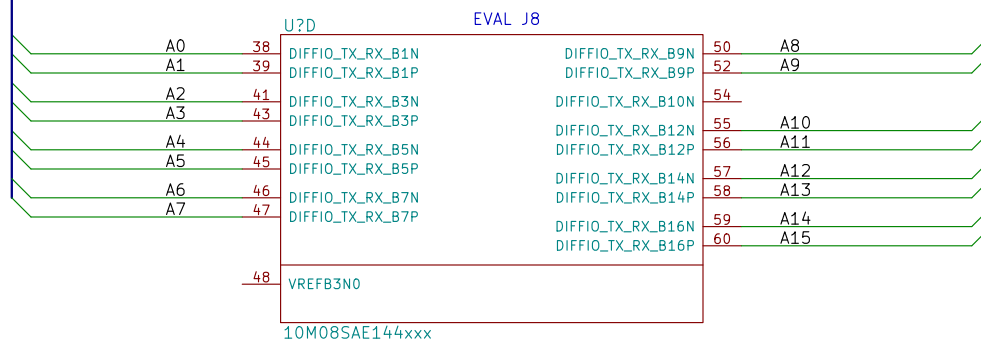
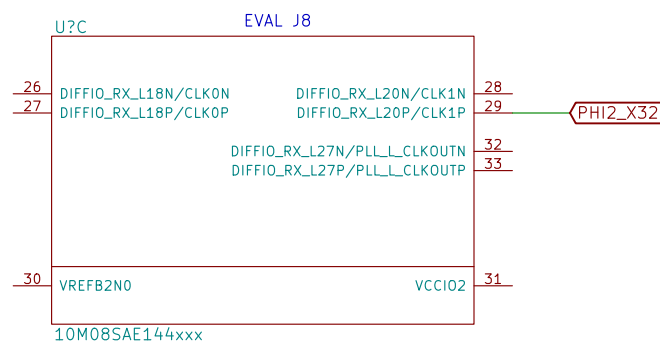
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NOTE: EARLY DRAFT, NOT FINAL!	
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Title: Atari PBI R:Fi -- TOP LEVEL	
Size: A	Date: 2017-03-31
KiCad E.D.A. kicad 4.0.6	Rev: A00
	Id: 1/5

DATA_BUS_3V



PHI2



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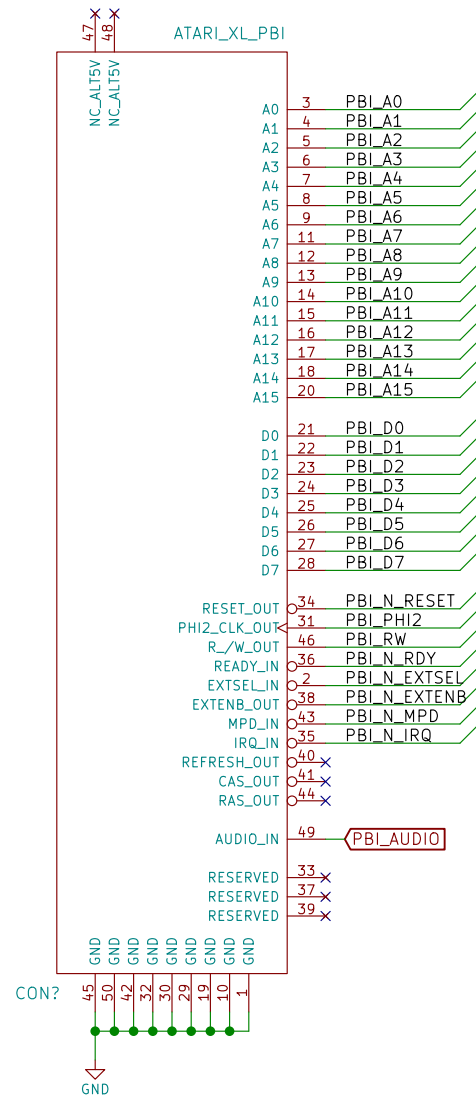
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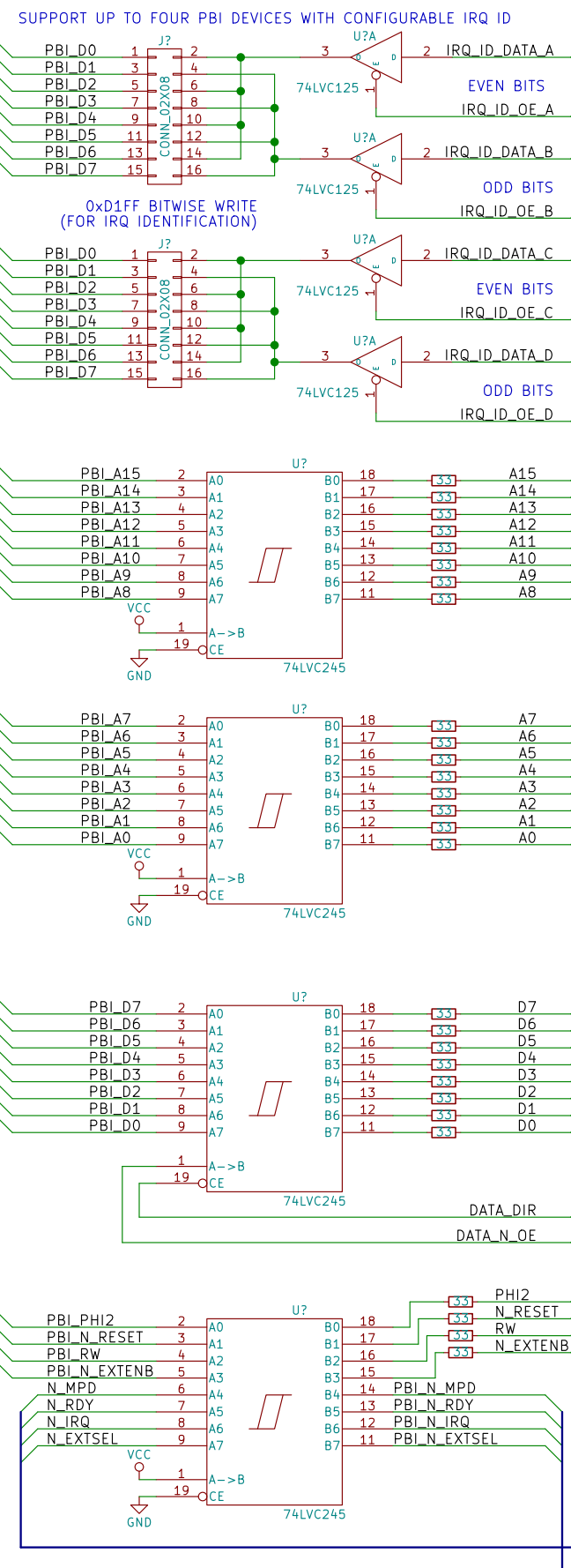
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Size: B Date: 2017-03-31
KiCad E.D.A. kicad 4.0.6

Rev: A00
Id: 2/5



TODO: IRQ is an open collector signal, add transistor



NOTE: ATARI PBI USES 5V LOGIC, FPGA AND OTHER DEVICES ARE ALL 3.3V
74LVCxxx DEVICES ARE USED FOR 5V/3.3V LEVEL SHIFTING
THEY RUN ON 3.3V VCC AND INPUTS ARE 5V TOLERANT

LINES DRIVING FPGA HAVE SERIES RESISTORS
PER ALTERA AN-447
https://www.altera.com/content/dam/altera-www/global/en_US/pdfs/literature/an/an447.pdf
FINAL VALUES TO BE CALCULATED - 33 OHM IN PROTOTYPE

TODO: CONSIDER SWITCHING TO 2.5V ON THIS SIDE OF DRIVERS

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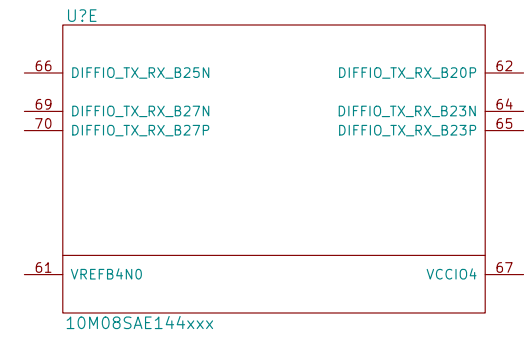
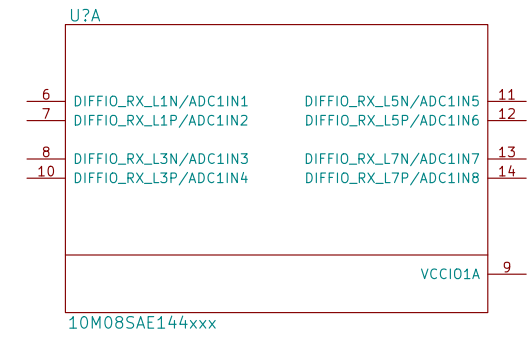
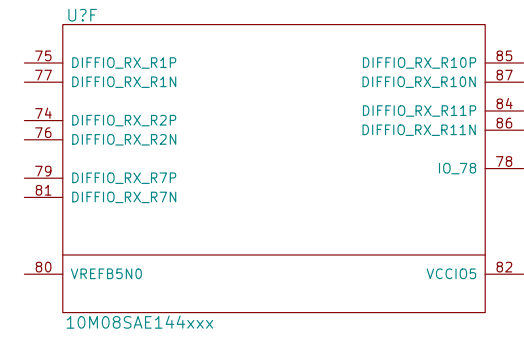
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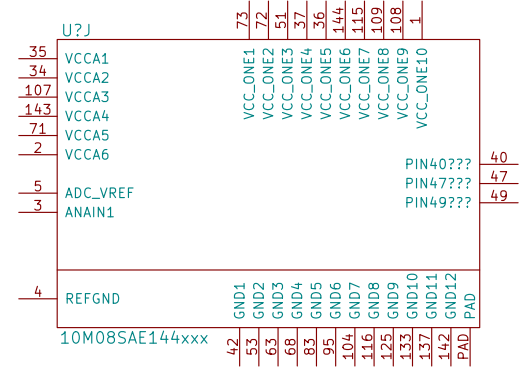
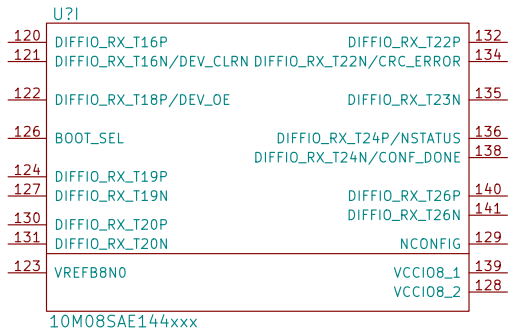
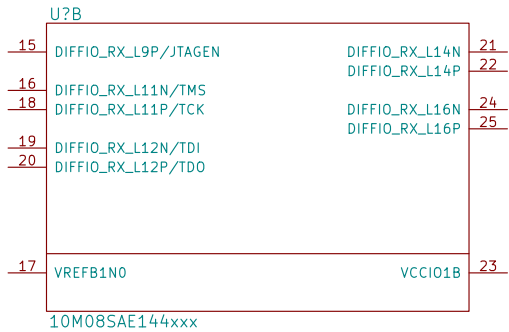
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Rev: A00
Id: 3/5

EVAL ARDUINO HDR



EVAL JTAG / LED / SWITCHES



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File: misc_fpga.sch

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Size: A4
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Rev:
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