

# ATARI NTSC Gtia reconstructed schematics

by Jorge Cwik [vapi.fxatari.com](http://vapi.fxatari.com)  
Version 1.0

The logic of these schematics was reverse engineered from the actual chip die layout. The style and graphic representation was done following the original schematics, barely readable, as present in the chip datasheet.

The graphic is close, but not exactly identical to the original schematics. Original signal names or similar, when they were readable at all, were used in most cases.

The actual logic on the chip layout is almost identical to the original schematics, at the extent that the latter is readable. The differences don't seem to be significant and without any relevance at the logical level.

The original schematics show an optional alternate chip select scheme, including a second pin. There are traces of this optional logic at the die layout, but no second chip select pad.









