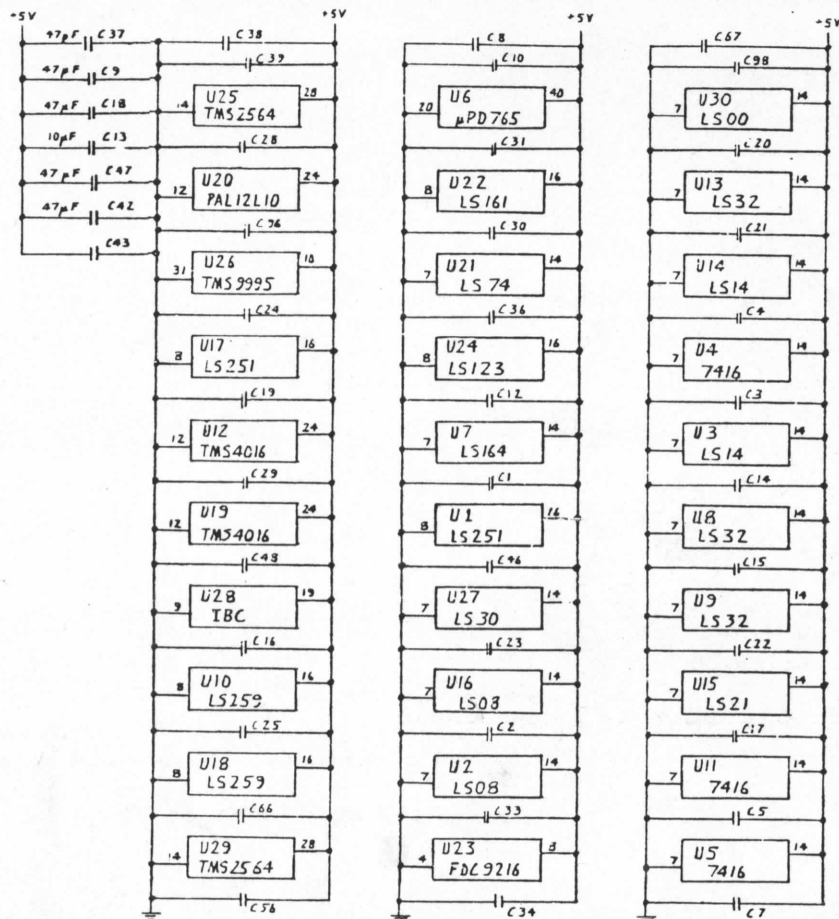


OPTIONAL, USED FOR 96TPI DRIVES ONLY.



ITEM NO.		PART NUMBER		DESCRIPTION		MATERIAL		REMARKS	
						PARTS LIST			
DIMENSION IDENT. NUMBER				UNLESS OTHERWISE SPECIFIED		DATE		TEXAS INSTRUMENTS	
USED		NOT USED		DIMENSIONS ARE IN MILLIMETERS		PART NAME		INCORPORATED	
				TOLERANCES ANGLES ± 1°		QTY		Computer Products Group	
				2 PLACE DECIMALS ±		QTY		Lubbock, Texas	
				1 PLACE DECIMALS ±		QTY		HEXBUS MINI-FLOPPY	
				DIMENSIONING IN ACCORDANCE WITH ANSI Y 14.5		QTY		DISK SYSTEM	
						QTY		DRAWING NO.	
						QTY		1056916	
						QTY		SCALE	
						QTY		SHEET 1 of 5	

FORMAL RELEASE

ALL BYPASS CAPS ARE .1 $\mu$ F

CLASS CODE	40
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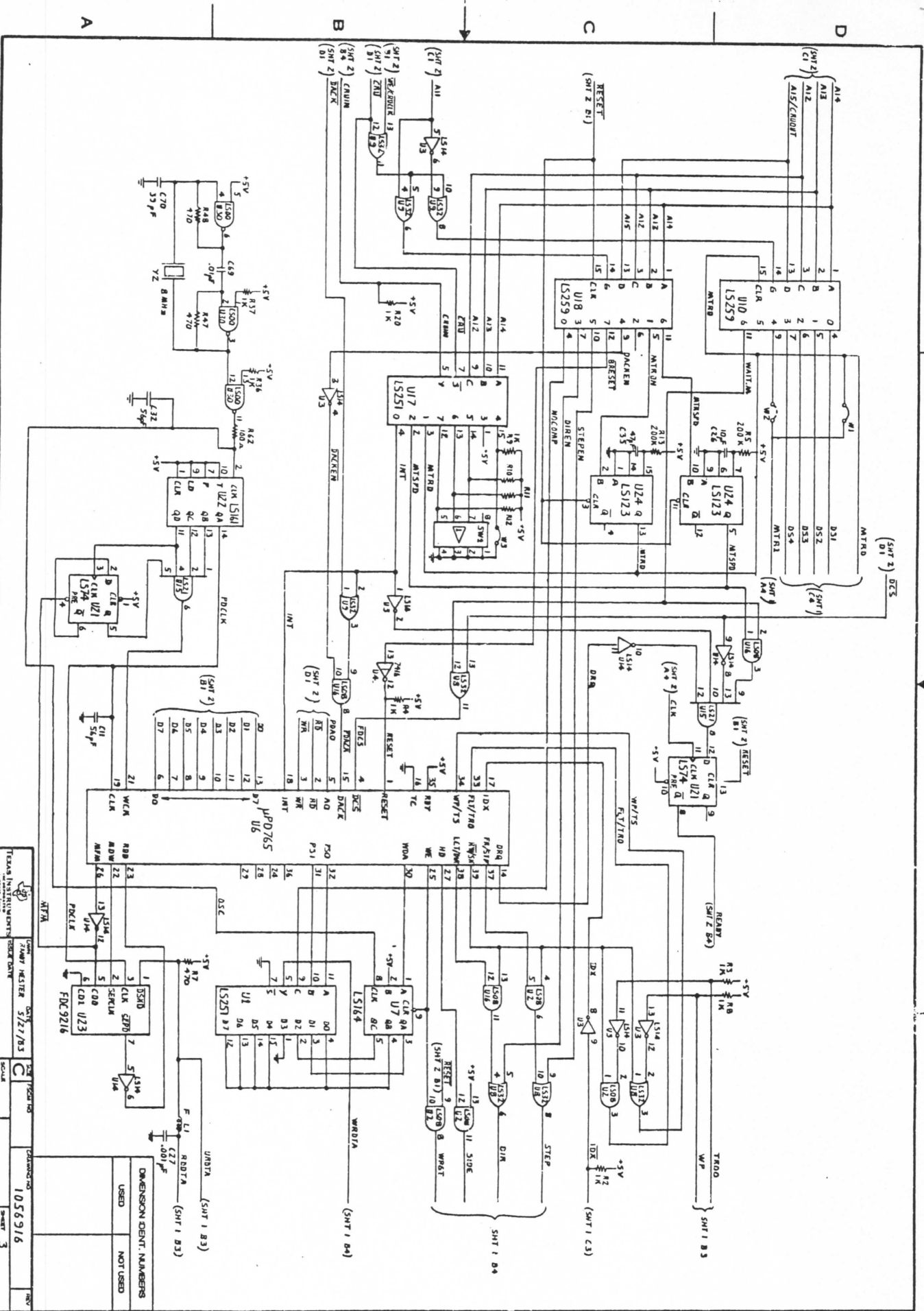


4

3

2

1



TELETYPE UNIT IDENT. 5127/83  
C  
1056916  
3



## HFD5 MEMORY MAP

## MEMORY SPACE

FAL12L10  
 HEXBUS DISK CONTROLLER - 2X 64K'S  
 7/27/83 2ND PASS PCB MOD. ROM EQUATIONS  
 TEXAS INSTRUMENTS, LUBBOCK, TX. DONALD DEVLIN  
 A15 A14 A13 A12 A11 /DBIN /MEM A3 A2 /WE /V2 GND  
 V1 /DACK /DCS /CRU /ROM1 /RAM /AZ /ROM2 /HCS /WR /RD VCC  
 ROM1 = MEM\*V1\*/A2\*/WE  
 ROM2 = MEM\*V1\*/A2\*/WE  
 RAM = MEM\*V1\*/A2\*/A3  
 DCS = MEM\*V1\*/A2\*/A3\*/A15\*/V2\*/A11\*/A14  
 HCS = MEM\*V1\*/A2\*/A3\*/A15\*/V2\*/A11\*/A13\*/A14  
 DACK = MEM\*V1\*/A2\*/A3\*/A15\*/V2\*/A11\*/A13\*/A14  
 AZ = MEM\*V1\*/A2\*/A3\*/A15\*/V2\*/A11\*/A12\*/A13\*/A14  
 CRU = MEM\*V1\*/A2\*/A3\*/A15\*/V2\*/A11\*/A12\*/A13\*/A14  
 RD = MEM\*V1\*/A2\*/A3\*/A15\*/V2\*/A11\*/A12\*/A13\*/A14  
 WR = MEM\*V1\*/A2\*/A3\*/A15\*/V2\*/A11\*/A12\*/A13\*/A14  
 +MEM\*V1\*/A2\*/A3\*/A15\*/V2\*/A11\*/A12\*/A13\*/A14

ADDRESS	DEVICE	FUNCTION
0000 - 1FFF	TMS4744	8K PROGRAM
2000 - 3FFF	TMS4744	8K PROGRAM
4000 - 5FFF	---	NOT USED
6000 - 7FFF	TMS4016	2K RAM
8000 - 9FFF	TMS4016	2K RAM
F000 - F07B	TMS 95	252 BYTES INTERNAL RAM
F0FC - F7DF	---	NOT USED
F7E0	UPD745	MAIN STATUS (READ)
F7E2	UPD745	DMA DATA (READ)
F7E4	UPD745	RESULT REGISTER (READ)
F7E6	---	NOT USED
F7E8	UPD745	COMMAND REGISTER (WRITE)
F7EA	UPD745	DMA DATA (WRITE)
F7EC	741374	READY LOGIC TRIGGER
F7EE	---	NOT USED
F7F0	IBC	RECEIVED DATA (READ)
F7F2	IBC	STATUS REGISTER (READ)
F7F4 - F7F6	---	NOT USED
F7F8	IBC	TRANSMIT DATA (WRITE)
F7FA	IBC	COMMAND REGISTER (WRITE)
F7FB - F7F9	---	NOT USED
FFFA	MEMORANTER	NOT USED
FFFB - FFFF	TMS3995	MAIL VECTOR INTERNAL RAM

OTHER WTBDE  
 CONTAINS DATA

## LOGICAL CRU SPACE

## OUTPUTS

ADDRESS	FUNCTION
17E0	PREDOMINATION
17E2	MOTOR ON TRIGGER
17E4	NOT USED
17E6	HEAD DIRECTION
17E8	DIRECT DATA ACKNOWLEDGE
17EA	HEAD STEP ENABLE
17EC	MOTOR START UP TIMER
17EE	UPD745 RESET
17F0	DISK 1 SELECT
17F2	DISK 2 SELECT
17F4	DISK 3 SELECT
17F6	DISK 4 SELECT
17F8	AUX. MOTOR CONTROL
17FA	DUMMY OUTPUT
17FC	READY LOGIC ENABLE

## INPUTS

ADDRESS	FUNCTION
17E0	INTERF. FROM UPD745
17E2	MOTOR ON SENSE
17E4	MOTOR SPEED SENSE
17E6	UNIFIED DIP SWITCH
17E8	DISK 1 TPE SELECT (SWITCH)
17EA	DISK 2 TPE SELECT (SWITCH)
17EC	DISK 3 TPE SELECT (SWITCH)
17EE	DISK 4 TPE SELECT (SWITCH)

## INTERNAL CRU FLAGS (7925)

1EEO-1E7E GENERAL FLAGS  
 1E8A MID FLAG

## DIMENSION IDENT. NUMBERS

USED NOT USED

TEXAS INSTRUMENTS	DATE	DESIGNED BY	1056916
RAMAY MASTER 5/21/83	SCALE	5	